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APPLICATION INFORMATION

Title Line One:: Low Voltage Input Current Mirror Circuit

Title Line Two:: And Method Total Drawing Sheets:: 10 Formal Drawings?:: Yes Application Type:: Utility Docket Number:: 1875.1090008

Secrecy Order in Parent Appl.?:: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 26111

CONTINUITY INFORMATION

This application is a:: CONTINUATION OF > Application One:: 10/288,418

Filing Date:: 11-06-2002

Which is a::CONTINUATION OF

>> Application Two:: 09/897,045

Filing Date:: 07-03-2001 Patent Number:: 6,531,923

Which is a::NON PROV. OF PROVISIONAL >>> Application Three:: 60/221,835

Filing Date:: 07-28-2000

And which is a:: NON PROV. OF PROVISIONAL

>>> Application Four:: 60/215,850 Filing Date:: 07-03-2000

Source:: PrintEFS Version 1.0.1